### **Introduction to Simulink**

Eman Alashwali

### **Objectives**

- Introduction to Simulink (MATLAB)
- Designing Digital Circuits using Simulink

# **Starting Matlab**

- From Start -> Matlab
- In the command line, type: simulink -> Press
  Enter and wait for a moment (it may take few seconds)

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- From File -> New -> Model
- You will get a blank window like this:



- To draw a circuit, we have 3 main categories for the components:
  - Input -> Source
  - Gate -> Logical and Bit Operation
  - Output -> Sink



- To draw a gate, click on Logic and Bit Operations.
- Click and drag the AND logical operator



- To change its shape and type (to OR, NAND, etc.) double-click on the gate
- Operator: AND / OR / NOR
- Icon shape: select distinctive to make the known shape for the gate operator

Function Block Parameters: Logical Operator						
Logical Operator						
Logical operators. For a single input, operators are applied across the input vector. For multiple inputs, operators are applied across the inputs.						
Main Signal Attributes						
Operator: AND						
Number of input ports:						
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Icon shape: distinctive						
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 After changing the logical operator to AND, and the shape to distinctive, the AND gate looks like:



- To add a constant input, from Source -> Constant
- Drag and drop the constant rectangle



- To change the constant value, double-click on the constant rectangle
- Draw a line by clicking on the constant -> click on the constant + shift -> click on the AND gate



- To display the output, click on the Sinks
- Drag and drop a Display

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- Draw a line from the AND gate to the Display rectangle
- Run your Model by click on the *Start Simulation* button
- You will see the result in the Display rectangle
- Try to change the constants values and watch the Display

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### **To Create a Subsystem**

- You need to create subsystems if you have complicated circuit. It allows you to hide the small details inside a subsystem
- Select the details you want to hide -> Right click -> choose "Create subsystem from selection"

# Subsystem Example

• I selected all the AND gates in this Multiplexer



• The subsystem hides all the AND gates in one componer<sup>+</sup> Output0 D1 Input1 Output1 D2 Input2 Output2 D3 Output3 Input3 D4 Final Output Subsystem Input4 D5 Output4 Input5 Output5

> D6 Input6

D7 Input7 3

Output6

Output7

# Lab Exercise

- Draw the following two functions
  - -AB + AC
  - A (B+C)
- Build the two expressions truth table
- Try to use the same constants value in the two circuits. *What do you observe ?*

#### HW#1

- Install Matlab Simulink in your own machine
- The SW is available in Student copy. You can buy it online from:

https://www.mathworks.com/store/link/products/st udent/?s tid=ac buy sv cta

- Cost USD \$ 89
- Practice drawing circuits

#### References

 King Abdulaziz University, faculty of computing & IT, CPIS – 210 lab manual Computer Architecture & Organization, pages (8 &9) by Abdul Rauf Malik